

Thiruppathi Kumar G
Lenovo Global Technology India Pvt Ltd | PCB Layout Engineer | Bangalore, India

BASIC INFORMATION

Experience in:	Hardware Industry
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Language:	English, and, Tamil.
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CAREER CONTOUR

I am unwavering, meticulous, and highly competent in the **Hardware Industry** I have a consistent record of transporting the best results-driven work with a proven ability to implement my work in an organized manner for **13+ years** of my successful career.

My expertise:- **Hardware Industry – PCB Design, Signal Integrity, High-Speed Layout, Constraint Management, EMI/EMC Compliance, DFM/DFA Expertise, Reverse Engineering, Back Drill Setup, Wave Routing, Layout Optimization, Design Reuse, Power Integrity, Cost Minimization, Tool Box Productivity.** I have in-depth knowledge of all the management ethics. I possess effective communication skills and am a team player with strong organizational, Logistical, and problem-solving abilities.

I have great exposure to working with large-scale organizations like **Sanmina Tech Services India Pvt. Ltd, Lenovo Global Technology India Pvt Ltd.** I have well-versed skills in analysis, problem-solving, and coordination, which make me so successful and dedicated. Strong influencing and negotiation skills coupled with a proven ability to think in and out of the box, generating new solutions.

I have commenced my profession as a **Trainee** and risen myself as a **PCB Layout Engineer** reputed company. I would like to see myself growing with the passing years of hard work and dedication. I have been awarded many accolades for my result-oriented hard work.

Armed with many competencies and work experiences, I am confident to carry forward any organization's vision & objectives with sufficient ease and dedication to my job responsibility.

EXPERIENCE

Jun'2024 – Till Date | Lenovo Global Technology India Pvt Ltd | As PCB Layout Engineer

Responsibility:-

- Leading PCB layout design projects, ensuring high-speed and mixed-signal design without signal integrity issues.
- Coordinating with various teams to ensure design consistency and accuracy in mechanical, electrical, and signal integrity aspects.
- Engaging with the mechanical team to analyze and fix mechanical requirements through EMN/DXF.
- Collaborating with testing teams to ensure signal integrity and power plane stability.
- Working closely with DFM/DFA teams to address manufacturing feedback.
- Ensuring designs meet EMI, EMC, DFM, DFA, and DFT requirements.

- Utilizing advanced knowledge of constraints and the constraint manager for design setups.
- Applying design efficiency techniques, including parallelism, wave routing, and back drilling setups.
- Preparing and following checklists from PDG recommendations to deliver projects on time.
- Scheduling processes, coordinating teams, and maintaining timelines for project delivery.
- Conducting thorough QC at various stages of the design process.
- Creating reusable design modules, especially for frequently used VR circuits.
- Adopting new technologies like the "Productivity Tool Box" to minimize production costs by consolidating multiple boards into one panel array.
- Presented at CADENCE LIVE 2020 on achieving high throughput and design efficiency using Allegro's Productivity Toolbox.
- Awarded Best Employee in 2021 and received an Award of Excellence in 2022 for outstanding performance and customer satisfaction.

PREVIOUS EXPERIENCE

- *Apr'2016 – Jun'2024 | Sanmina Tech Services India Pvt. Ltd | As Senior PCB Layout Engg*
- *Apr'2014 – Apr'2016 | Schlumberger Pvt. Ltd | As PCB Layout Engineer*
- *Sep'2011 – Mar'2014 | Virya Info Tech | As PCB Layout Engineer*

ACHIEVEMENTS

- Selected as Best Employee from the IDC Design Team and awarded a bonus for outstanding performance in 2021.
- Received the Award of Excellence in 2022 for exceptional skill and meeting customer requirements.
- Successfully presented at CADENCE LIVE 2020 on "Achieving High Throughput and Design Efficiency with Allegro Productivity Toolbox."
- Contributed to significant cost-saving initiatives by implementing design efficiency techniques, and reducing production costs through board consolidation.
- Consistently met project timelines and delivered high-quality PCB designs while adhering to EMI/EMC, DFM, DFA, and DFT standards.
- Started my career as a Trainee and rose as a PCB Layout Engineer.

ACADEMIC FORTE

- B.E Electrical & Electronics Engineering from Anna University in 2009.

Training & Certifications:

- Utilized Placement Replicate by creating a "Module" in the schematic, enabling the efficient import of "Derived Circuits" into the layout during net-list import.
- Achieved cost reduction by one-third through penalization using the Productivity Tool Box, consolidating three boards into a single panel.
- Gained proficiency in Design Comparison using IPC-2581 standards.
- Hands-on experience with Symphony – Team Design, facilitating collaborative PCB design projects.

TECHNICAL PROFICIENCY

Well versed with

- Ms office
- Windows
- Internet application

Advice for Contacting:

I'm always open to conversation, networking with like-minded professionals, or discussing future prospects. Connect with me on Linked In or contact me at g.thirupathi88@gmail.com